10/60\$\$ 844 TRANSMITTAL LETTER Docket No. (General - Patent Issued) BUR920030070 U.S. Patent No. \* **Issue Date** 6,927,590 B2 August 9, 2005 Title: METHOD AND CIRCUIT FOR TESTING A REGULATED POWER SUPPLY IN AN INTEGRATED CIRCUIT **COMMISSIONER FOR PATENTS:** Transmitted herewith is: **Certificate of Correction** Certificate **Postcard** SEP 2 6 2005

No fee required below. Errors were made by the U.S. Patent Office

of Correction

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Jack P. Fredram Signature

Dated: 09-19-2005

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P17A/REV04

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: 6,927,590 B2

DATED : August 9, 2005

INVENTOR(S): Iadanza

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

## Column 8

Line 5, delete "cads" and insert --pads--Line 54, delete "herein" and insert --wherein"

## Column 10

Line 25, delete "around" and insert -- ground--

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PATENT NO. 6,927,590 B2

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